

**Amendments to the Specification:**

Please replace paragraph 34 with the following amended paragraph:

-- Fig. 3 shows a power device 54 configured to handle relatively high forward and reverse blocking voltages according to one embodiment of the present invention. The device 54 is a thyristor like the device 50 and has similar structures. Accordingly, the same numerals are used to denote the corresponding structures in the two devices. Generally, an n+ region (not shown) 7(b) is defined inside the layer 5 for a thyristor. --